

HC 041 U.S. PRO
09/739743
12/26/00

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

<p><i>Wier</i></p> <p>O.I.P.E. <i>Wier</i></p> <p>SCANNED <i>Wier</i> Q.A. <i>Wier</i></p>	<p>PATENT DATE</p>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/739743	F	438	787	2812-3052	COLEMAN

TITLE	APPLICANTS
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3. Secretary	3. Mr. J. Edgar Hoover
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Sun-00 Kim

Method for manufacturing interlayer dielectric layer in semiconductor device

PTO-2040
12/99[illegible]

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